



David M. Solak
4805 Maple Hill Drive
Seven Hills, Ohio 44131-5916

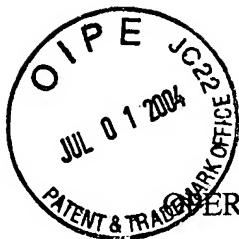
BEST AVAILABLE COPY

Dear Commissioner for Patents, Patent Examiner,

I (David M. Solak) respectfully certify that the discs marked COPY1 and COPY2 are identical in the nature of data provided on each disc. Each disc was made with the same computer and software (Ahead Nero) within a ten-minute window. All info contained on the discs is original to David M. Solak except for the operating systems and software from which the text and drawings were generated.

Signed David M. Solak Date 06/27/04

Engineering Technologist/Inventor



OPERATING SYSTEM USED: WINDOWS XP HOME VERSION

SOFTWARE: MS-Word .doc file
MS-Word .txt file
TurboCad .TCW files
AutoCad Native formats .dwg files

Inventor: David M. Solak
4805 Maple Hill Drive
Seven Hills, Ohio 44131-5916
d.solak@sbcglobal.net

Title of Invention: RF SMOKE SENSING SYSTEM WITH HEAT/SMOKE SENSING
CHRISTMAS ORNAMENT TRANSMITTER

22387 U.S.PTO
10/792357
030404

Directory of U.S. PTO RF, CD-ROM
Copy 1/Copy 2

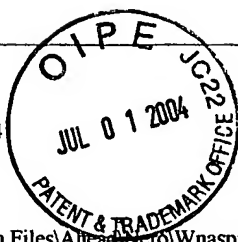
(Hand Copied from DOS Directory)

06/26/2004	05:10PM	9,714	BLOCKDIA1 (AutoCad Native)
03/03/2004	10:56AM	26,112	BLOCKDIA1.TCW (TurboCad)
06/26/2004	05:10PM	9,606	BLOCKDIA2
03/03/2004	10:57AM	26,112	BLOCKDIA2.TCW
06/26/2004	05:11PM	9,606	BLOCKDIA3
06/27/2004	12:45AM	26,112	BLOCKDIA3.TCW
06/26/2004	05:11PM	20,577	BLOCKDIA4
03/03/2004	10:59AM	32,768	BLOCKDIA4.TCW
06/26/2004	05:14PM	139,535	HOUSING1
03/03/2004	11:10AM	41,984	HOUSING1.TCW
06/26/2004	05:15PM	132,026	HOUSING2
03/03/2004	11:08AM	40,448	HOUSING2.TCW
06/26/2004	05:13PM	369,068	SCHEMATIC
03/03/2004	11:13AM	346,624	SCHEMATIC.TCW
06/26/2004	05:03PM	6,855	TEXT.TEXT.txt
06/26/2004	04:58PM	28,160	TEXT.doc

16 File (s) 1,265,307 bytes
0 Dir (s) 0 bytes free

Owner

1502-4120-1246-0757-1137-5514



Windows XP 5.1

WinAspi: -

ahead WinASPI: File 'C:\Program Files\Ahead\Nero\Wnaspi32.dll': Ver=2.0.1.50, size=131072 bytes, created 6/25/2002 12:34:54 PM

Nero Version: 5.5.9.9 (Nero Express)

Recorder: <HL-DT-ST CD-RW GCE-8525B>Version: 1.03 - HA 1 TA 0 - 5.5.9.9

Adapter driver: <atapi> HA 1

Drive buffer : 2048kB

Bus Type : default (0) -> ATAPI, detected: ATAPI

CD-ROM: <HL-DT-ST CD-RW GCE-8525B>Version: 1.03 - HA 1 TA 0 - 5.5.9.9

Adapter driver: <atapi> HA 1

Bus Type : default (0) -> ATAPI, detected: ATAPI

=== Scsi-Device-Map ===

DiskPeripheral : HDS722540VLAT20 atapi Port 0 ID 0 DMA: On

CdRomPeripheral : HL-DT-ST CD-RW GCE-8525B atapi Port 1 ID 0 DMA: On

BEST AVAILABLE COPY

AutoRun : 1

Excluded drive IDs:

CmdQueuing : 1

CmdNotification: 2

WriteBufferSize: 34603008 (0) Byte

ShowDrvBufStat : 0

EraseSpeed : 0

BUFE : 0

Physical memory : 253MB (259888kB)

Free physical memory: 55MB (57060kB)

Memory in use : 78 %

Uncached PFiles: 0x0

Use Static Write Speed Table: 0

Use Inquiry : 1

Global Bus Type: default (0)

Check supported media : Enabled (1)

Wizard: On

27.6.2004

CD-ROM (ISO)

12:53:23 PM #1 Text 0 File Isodoc.cpp, Line 6003

Iso document burn settings

Determine maximum speed : FALSE

Simulate : FALSE

Write : TRUE

Finalize CD : TRUE

Multisession : FALSE

Burning mode : TAO

Mode : 1

ISO Level : 1 (Max. of 11 = 8 + 3 char)

Character set : ISO 9660

Joliet : TRUE

Allow pathdepth more than 8 directories : TRUE

Allow more than 255 characters in path : TRUE

Write ISO9660 ;1 file extensions : TRUE

12:53:23 PM #2 Phase 90 File dlgrnst.cpp, Line 1566

Buffer Underrun Protection activated

12:53:23 PM #3 Text 0 File Reader.cpp, Line 118

Reader running

12:53:23 PM #4 Text 0 File Writer.cpp, Line 134

Writer HL-DT-ST CD-RW GCE-8525B running

12:53:23 PM #5 ISO9660GEN -11 File geniso.cpp, Line 4488

First writeable address = 0 (0x00000000)

12:53:23 PM #6 Text 0 File Burncd.cpp, Line 3542
Turn on Track-At-Once, using CD-R/RW media

12:53:23 PM #7 Text 0 File ThreadedTransferInterface.cpp, Line 663
Setup items (original item values)
0: TRM_DATA_MODE1 (CTransferItem)
2 indices, index0 (150) not provided
original CD pos #0 + 805 (805) = #805/0:10.55
relocatable, CD pos for caching/writing not required/required, no patch infos

12:53:24 PM #8 Text 0 File DlgWaitCD.cpp, Line 199
Last possible write address on media: 359844 (79:59.69)
Last address to be written: 804 (0:12.54)

12:53:24 PM #9 Text 0 File DlgWaitCD.cpp, Line 204
Write in overburning mode: FALSE

12:53:24 PM #10 Text 0 File DlgWaitCD.cpp, Line 1403
Recorder: HL-DT-ST CD-RW GCE-8525B;
CDRW code: 00 97 15 17; OSJ entry from: Ritek Co.
ATIP Data:
Special Info [hex] 1: C0 00 90, 2: 61 0F 11 (LI 97:15.17), 3: 4F 3B 46 (LO 79:59.70)
Additional Info [hex] 1: 00 00 80 (invalid), 2: 00 80 00 (invalid), 3: 00 80 80 (invalid)

12:53:24 PM #11 Text 0 File ThreadedTransferInterface.cpp, Line 815
Prepare recorder HL-DT-ST CD-RW GCE-8525B for write in TAO
DAO infos:
=====

MCN:
TOCTYPE: 0x0 Close CD
Tracks 1 to 1:
TRM_DATA_MODE1, 2048/0x0, ISRC "", FilePos 0 307200 1955840

12:53:24 PM #12 Text 0 File ThreadedTransferInterface.cpp, Line 843
Removed 2 run-out blocks from end of track 1.

12:53:24 PM #13 Text 0 File ThreadedTransferInterface.cpp, Line 663
Setup items (after recorder preparation)
0: TRM_DATA_MODE1 (CTransferItem)
2 indices, index0 (150) not provided
original CD pos #0 + 805 (805) = #805/0:10.55
relocatable, CD pos for caching/writing not required/required, no patch infos
-> TRM_DATA_MODE1, 2048, config 0, wanted index0 0 blocks, length 803 blocks [HL-DT-ST CD-RW GCE-8525B]

12:53:24 PM #14 Phase 24 File dlgbrnst.cpp, Line 1566
Caching of files started

12:53:45 PM #15 Phase 25 File dlgbrnst.cpp, Line 1566
Caching of files completed

12:53:45 PM #16 Phase 36 File dlgbrnst.cpp, Line 1566
Burn process started at 52x (7,800 KB/s)

12:53:45 PM #17 Text 0 File ThreadedTransferInterface.cpp, Line 1947
Verifying CD position of item 0 (relocatable, CD pos, no patch infos, orig at #0): write at #0

12:53:45 PM #18 Text 0 File Mmc.cpp, Line 16973
Set BUFE: supported -> ON

12:53:56 PM #19 Text 0 File ThreadedTransfer.cpp, Line 222
all writers idle, stopping conversion

12:54:14 PM #20 Phase 37 File dlgbrnst.cpp, Line 1566
Burn process completed successfully at 52x (7,800 KB/s)

12:54:14 PM #21 Phase 78 File dlgbrnst.cpp, Line 1566
Data verification started

BEST AVAILABLE COPY